

Figure 1

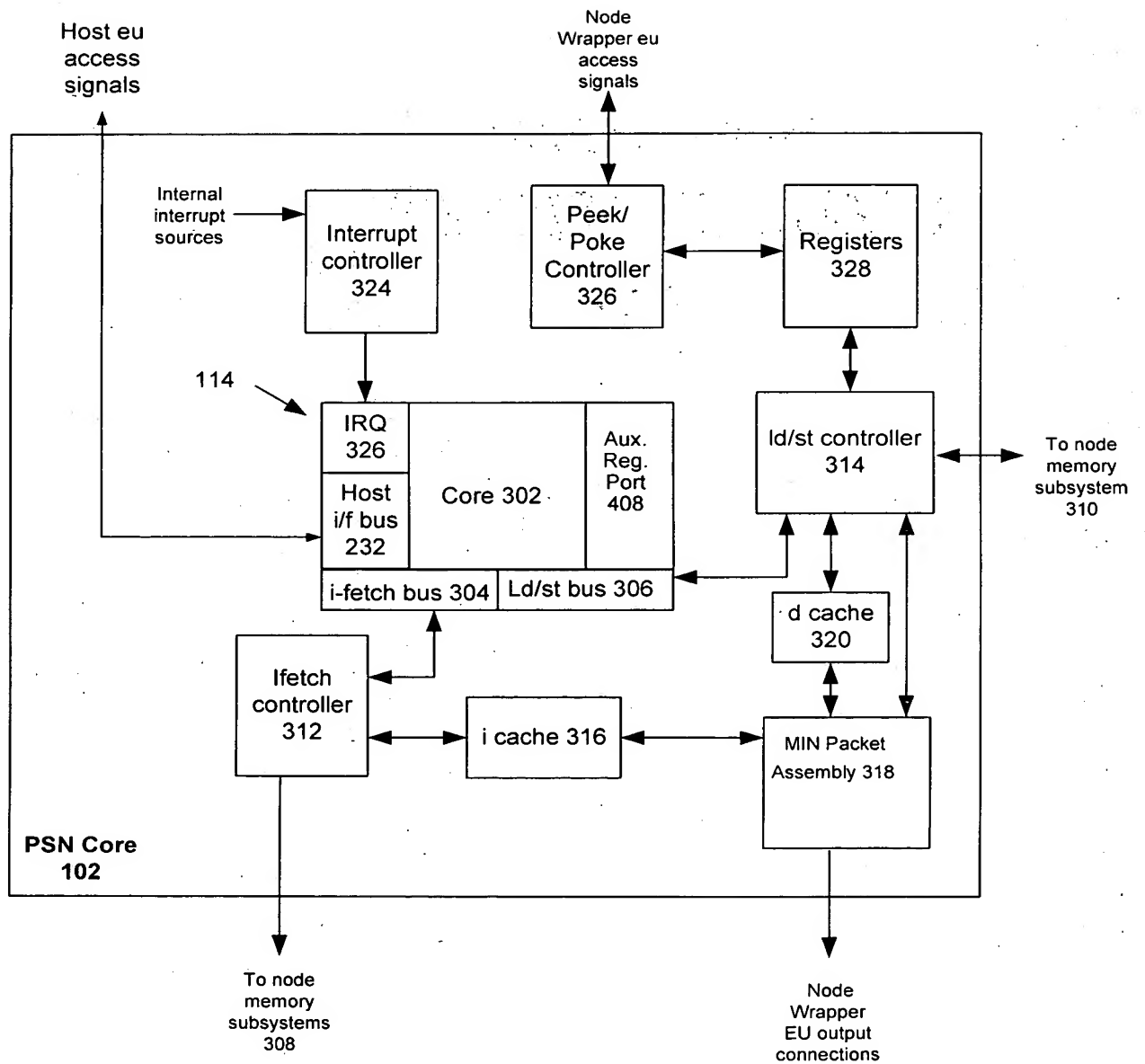


Figure 3

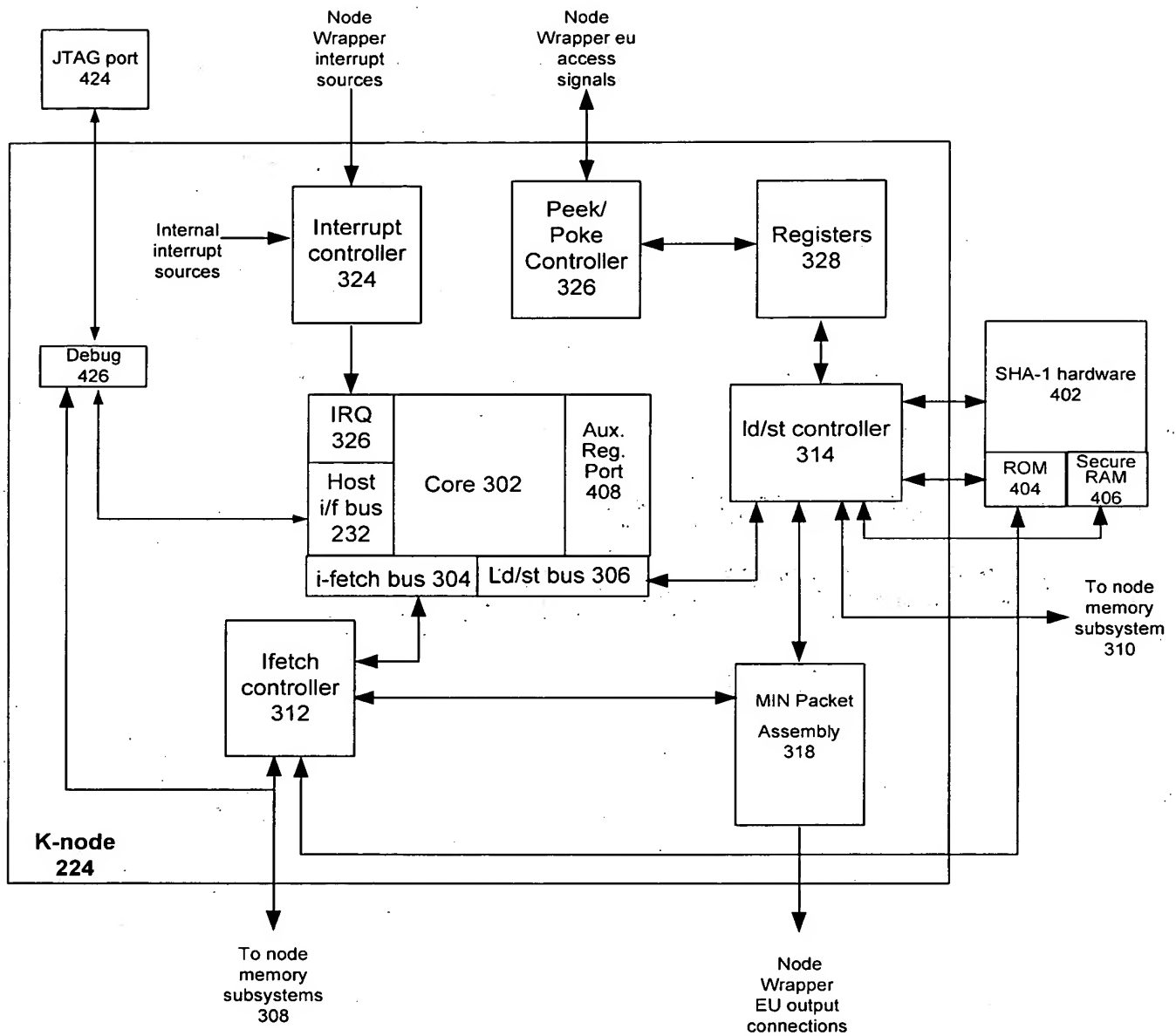


Figure 4

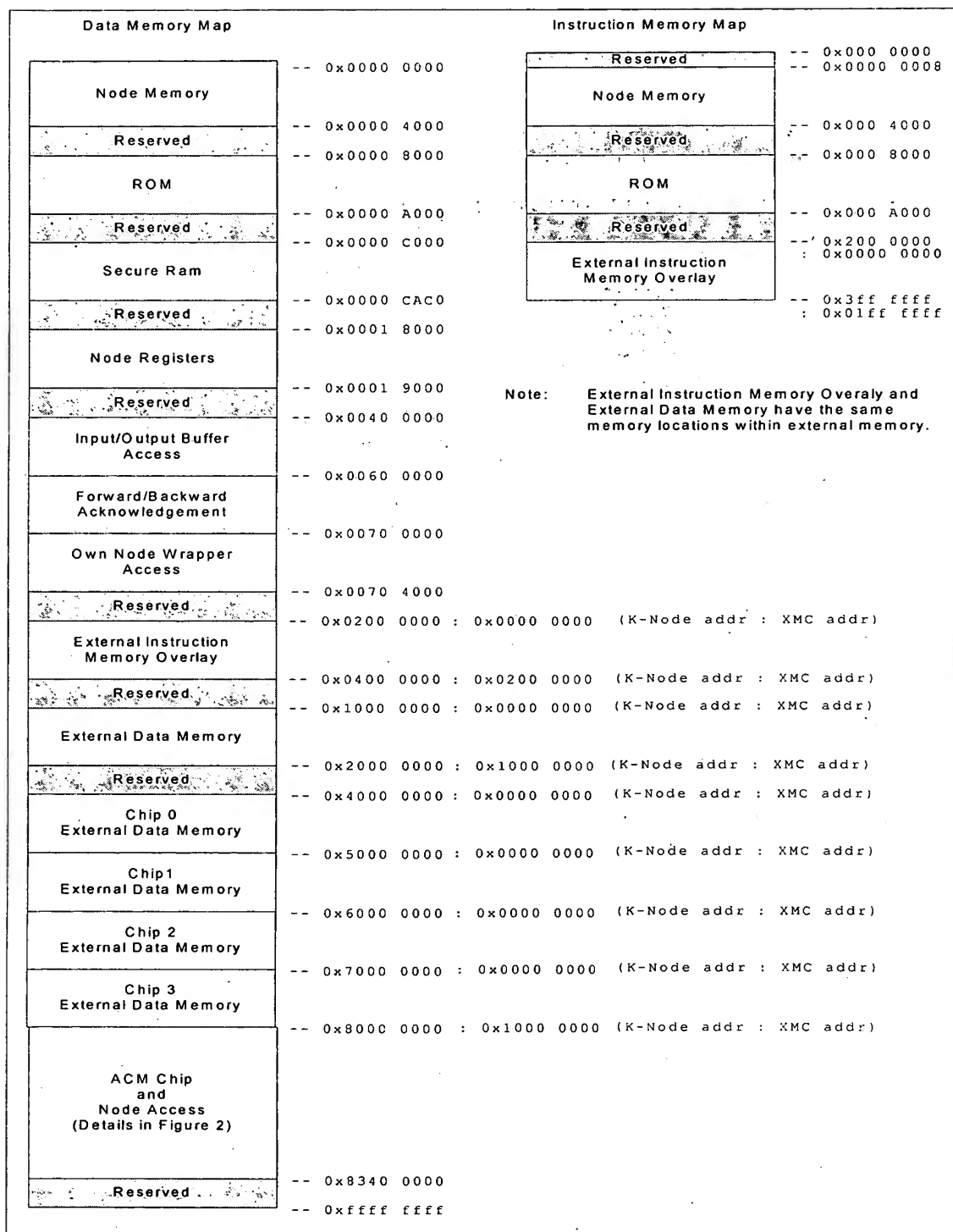


Figure 5A

Data Access

nodal memory space	0x0000_0000
	0x0000_4000
	0x1_8000
PSN registers	0x1_9000
	0x40_0000
Input/output buffer access	0x60_0000
Forward/backward ack	0x70_0000
Self node node wrapper	0x70_4000
	0x200_0000 : 0x0000 0000 (K-Node addr : XMC addr)
External memory overlay	0x400_0000 : 0x0200 0000 (K-Node addr : XMC addr)
	0x1000_0000 : 0x0000 0000 (K-Node addr : XMC addr)
External memory	0x2000_0000 : 0x1000 0000 (K-Node addr : XMC addr)
	0x4000_0000 : 0x0000 0000 (K-Node addr : XMC addr)
External memory0(mra)	0x5000_0000 : 0x0000 0000 (K-Node addr : XMC addr)
External memory1(mra)	0x6000_0000 : 0x0000 0000 (K-Node addr : XMC addr)
External memory2(mra)	0x7000_0000 : 0x0000 0000 (K-Node addr : XMC addr)
External memory3(mra)	0x8000_0000 : 0x1000 0000 (K-Node addr : XMC addr)
	0xffff_fff

Instruction Access

nodal memory space	0x000_0000
	0x000_4000
	0x200_0000
External memory	: 0x0000 0000
	0xffff_fff
	: 0x01ff ffff

Figure 5B

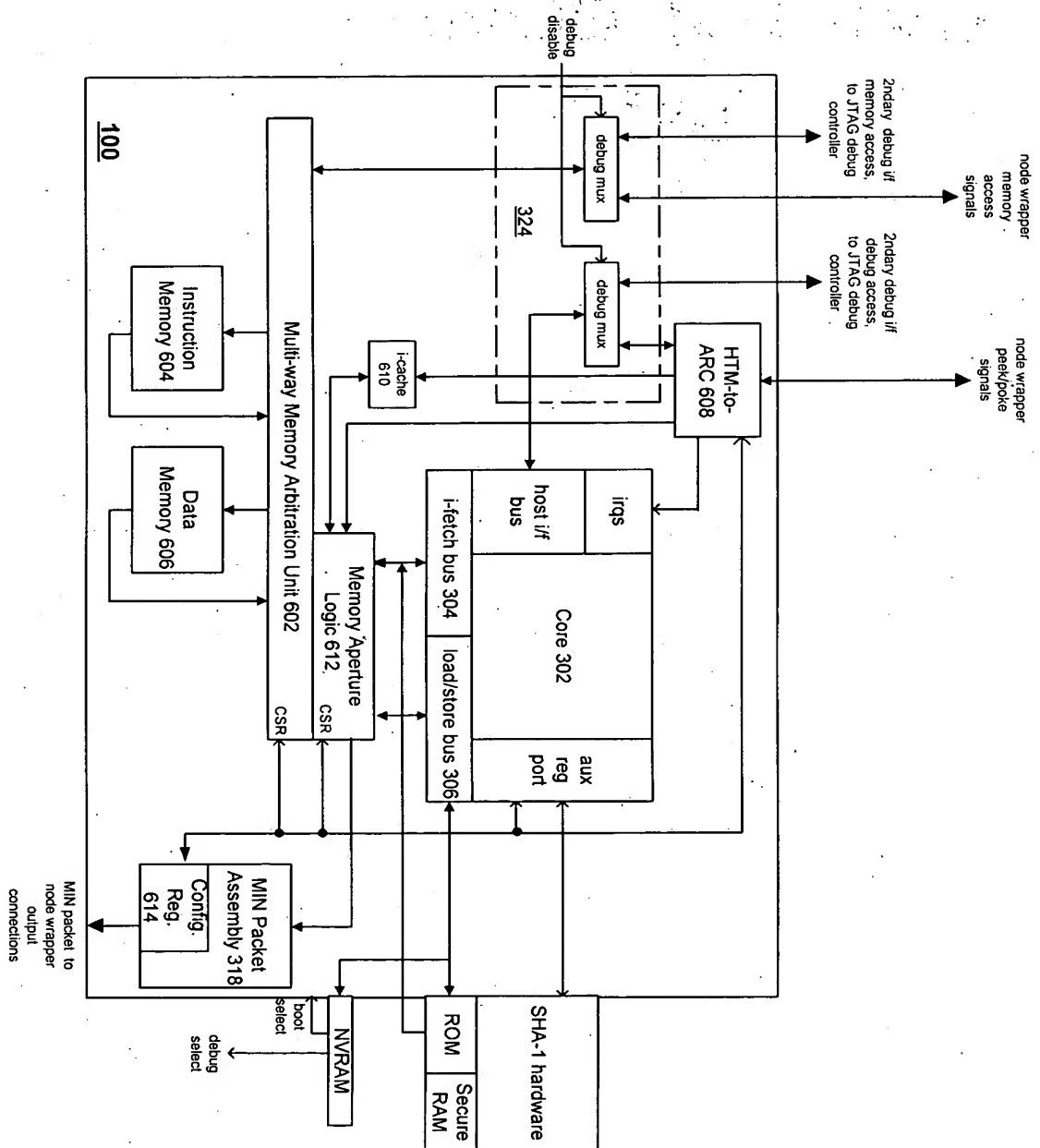


Figure 6

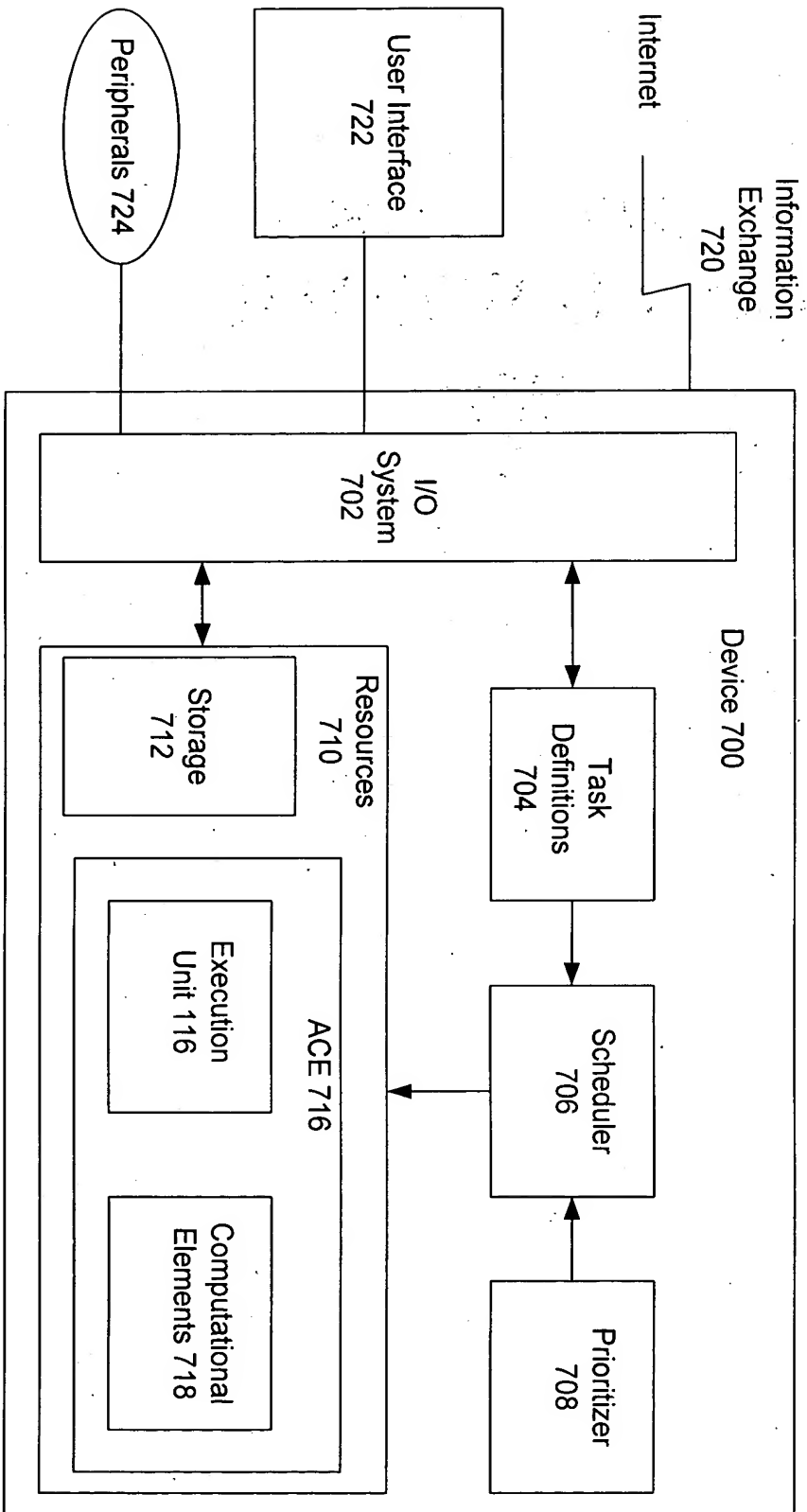


Figure 7

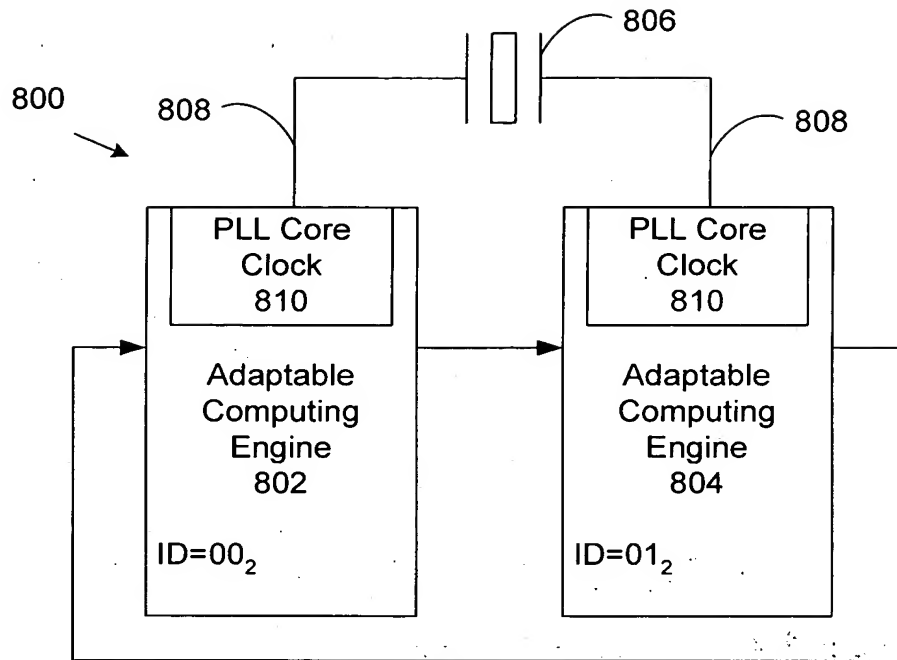


Figure 8A

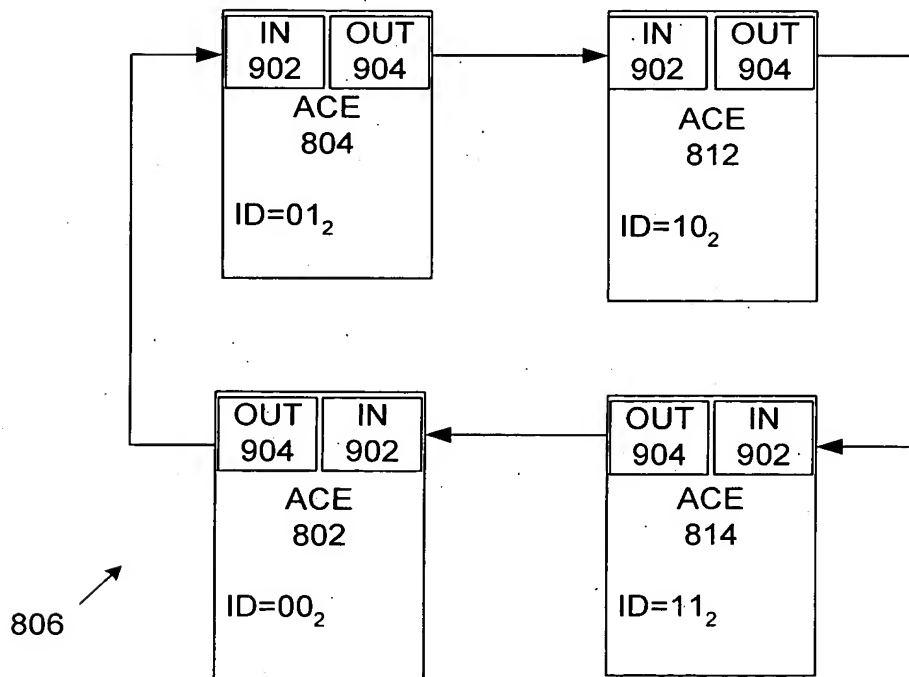


Figure 8B

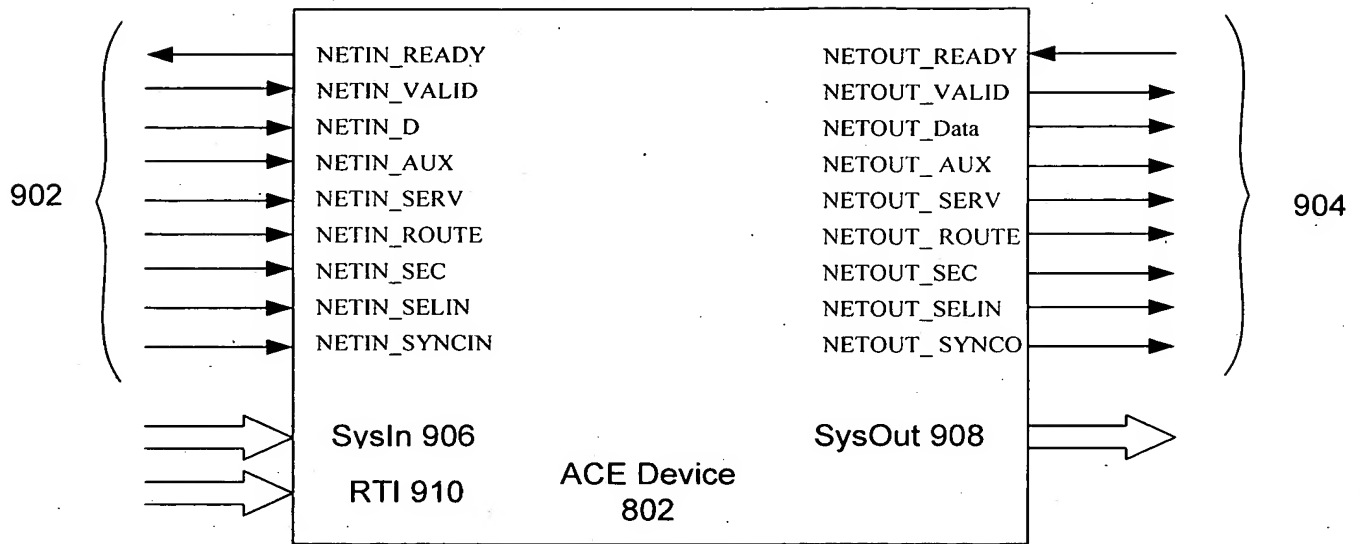


Figure 9

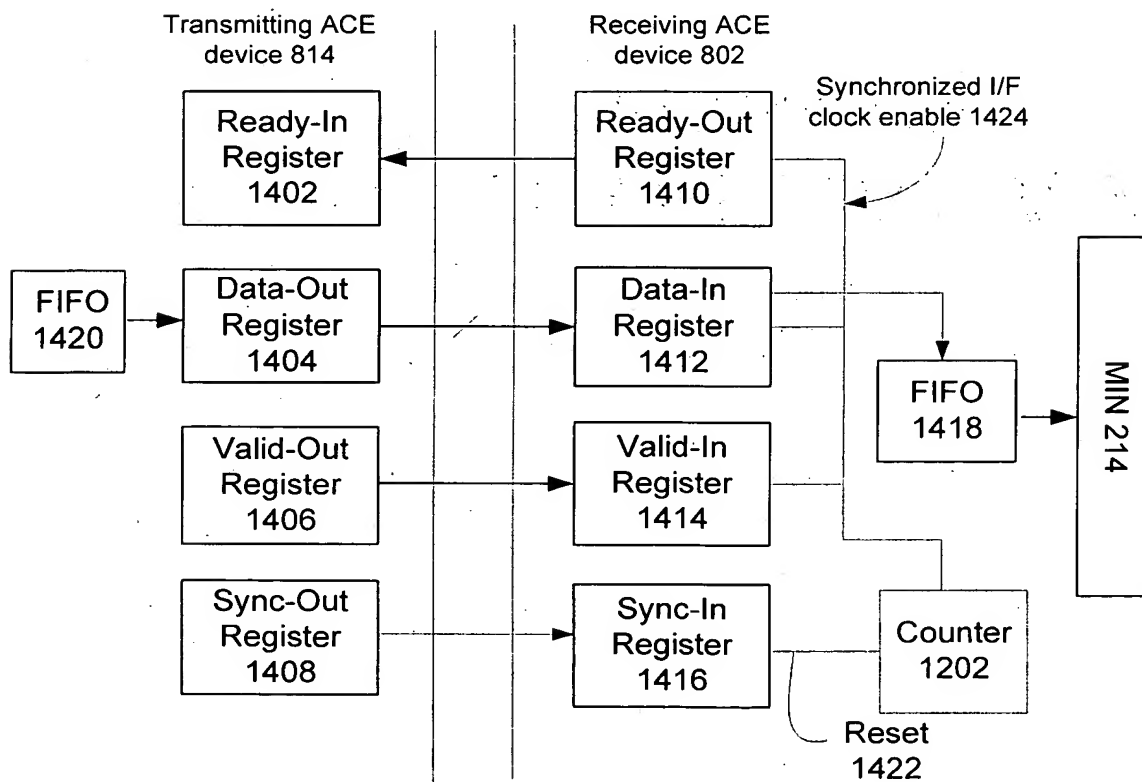


Figure 14

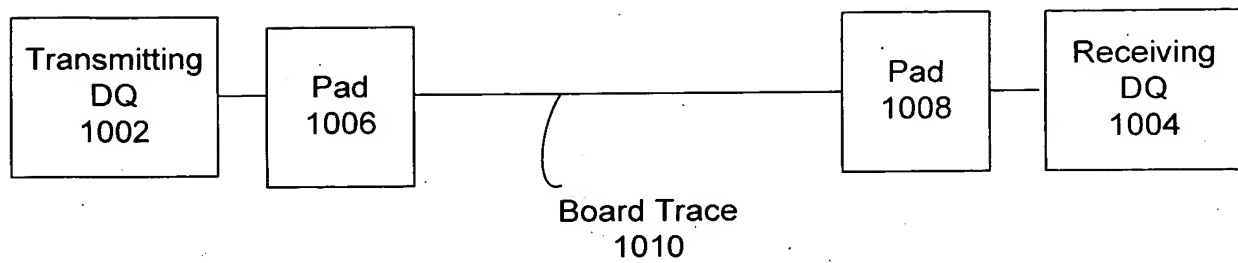


Figure 10

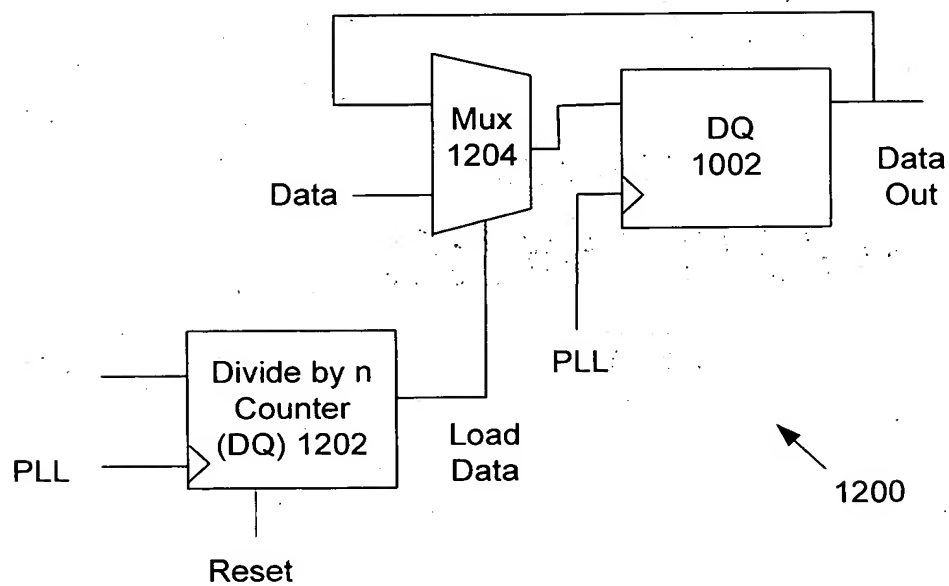


Figure 12

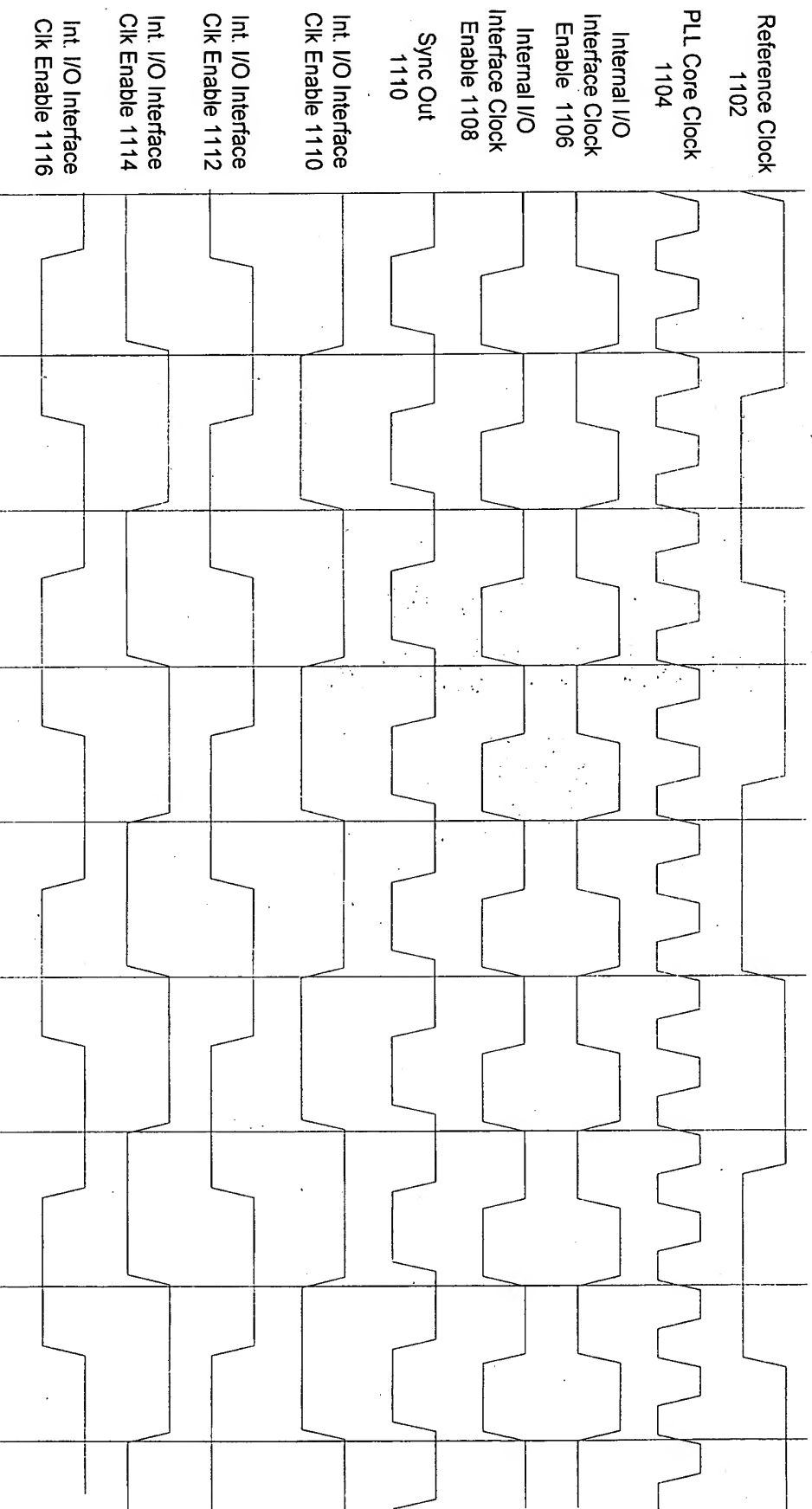


Figure 11

NetIn - NetOut INTERFACE (half-speed interface)

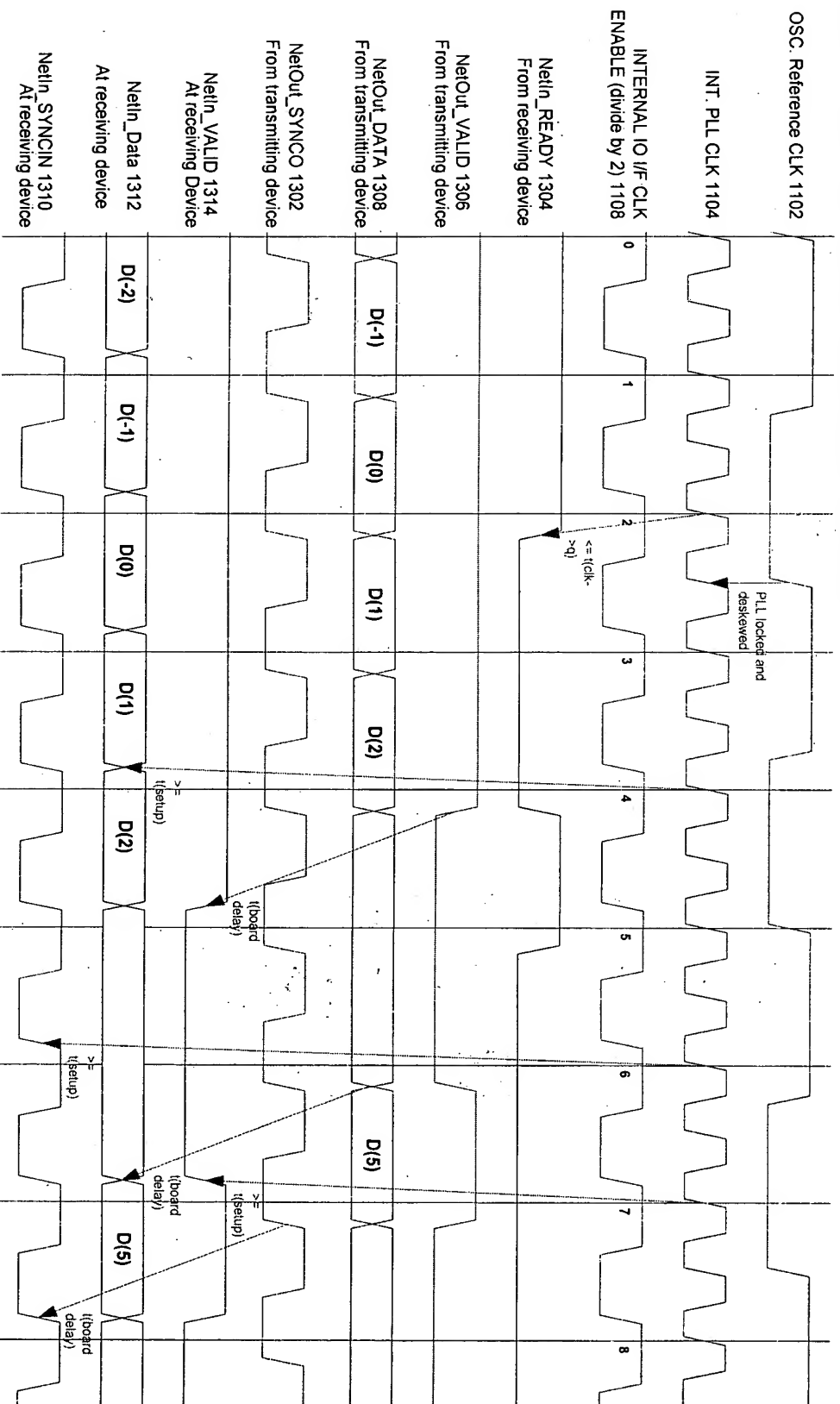


Figure 13A

SYSIN INTERFACE (full-speed)

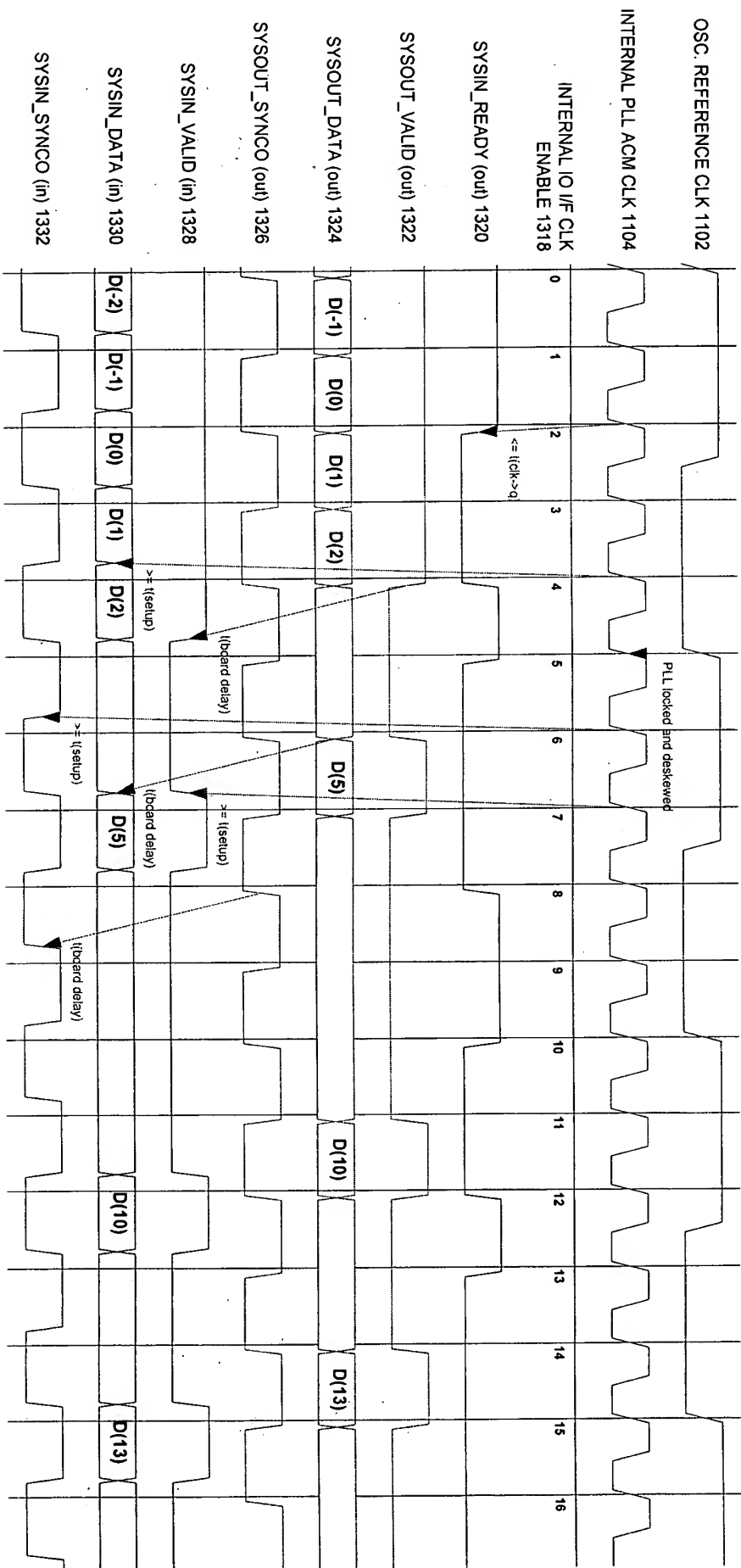


Figure 13B

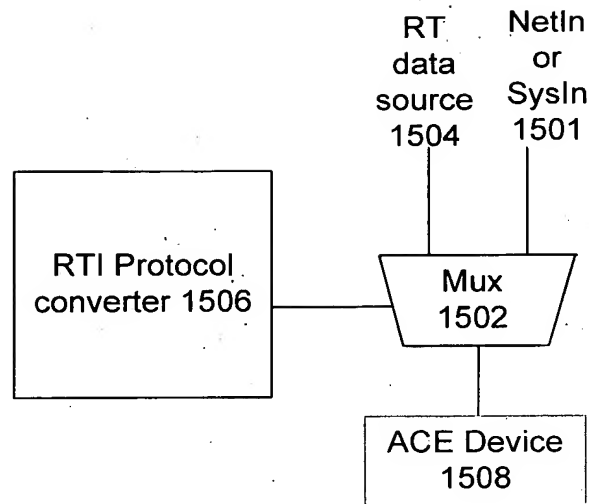


Figure 15

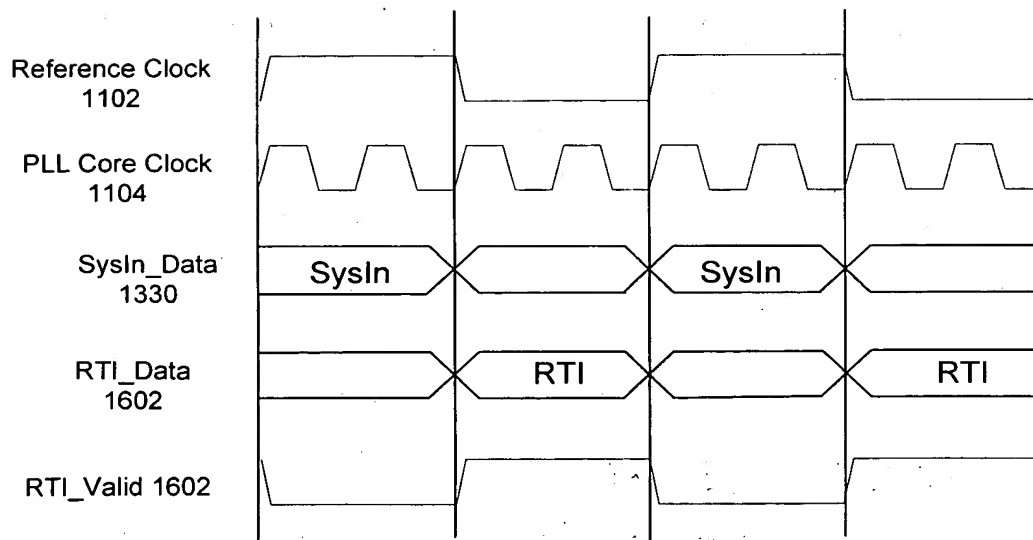


Figure 16